

Notice of References Cited		Application No.	Applicant(s)
		08/984,562	Mailloux et al.
Examiner	H. Kim	Group Art Unit	2751
		Page	1 of 1

U.S. PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	5652724	7/97	Manning	365	189.05
B	5610864	3/97	Manning	365	193
C	5526320	6/96	Zagar et al.	365	233.5
D	5392239	2/95	Margulis et al.	365	744 212
E	5175835	12/92	Beighe et al.	711	212
F	5146582	9/92	Begun	395	500
G	4851990	7/89	Johnson et al.	710	395 280
H	4519028	5/85	Olsen et al.	395	559
I					
J					
K					
L					
M					

FOREIGN PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

NON-PATENT DOCUMENTS

*	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Micron Technology Inc., "1995 DRAM DATA Book" pp4-1 thru 4-42	12/95
V	S3 Incorporated, "S3 Burst Mode DRAM", Product Overview, 2 pages	6/93
W	OKI Electric Ind. Co., Ltd., "Burst DRAM Function & Pinout", JC42.3, Albuquerque, 2nd presentation, Item #619,	9/94
X		

* A copy of this reference is not being furnished with this Office action.
 (See Manual of Patent Examining Procedure, Section 707.05(a).)

Part of Paper No. 4